Resolution Plot PAVIA S2

Wafer No.: 2083-9 **Thickness:** 74 um

JUNCTION

DET LINE:	194	KeV
SYSTEM:	160	KeV
CAL:	109	KeV

OHMIC

DET LINE:	195	KeV
SYSTEM:	160	KeV
CALC:	111	KeV

Source Am 241

Rise Time 2

Flat Top 0

VFS=512

Channels

0   1024   2048   3072   4096

0 52 104 156 208 260 312 364 416 468
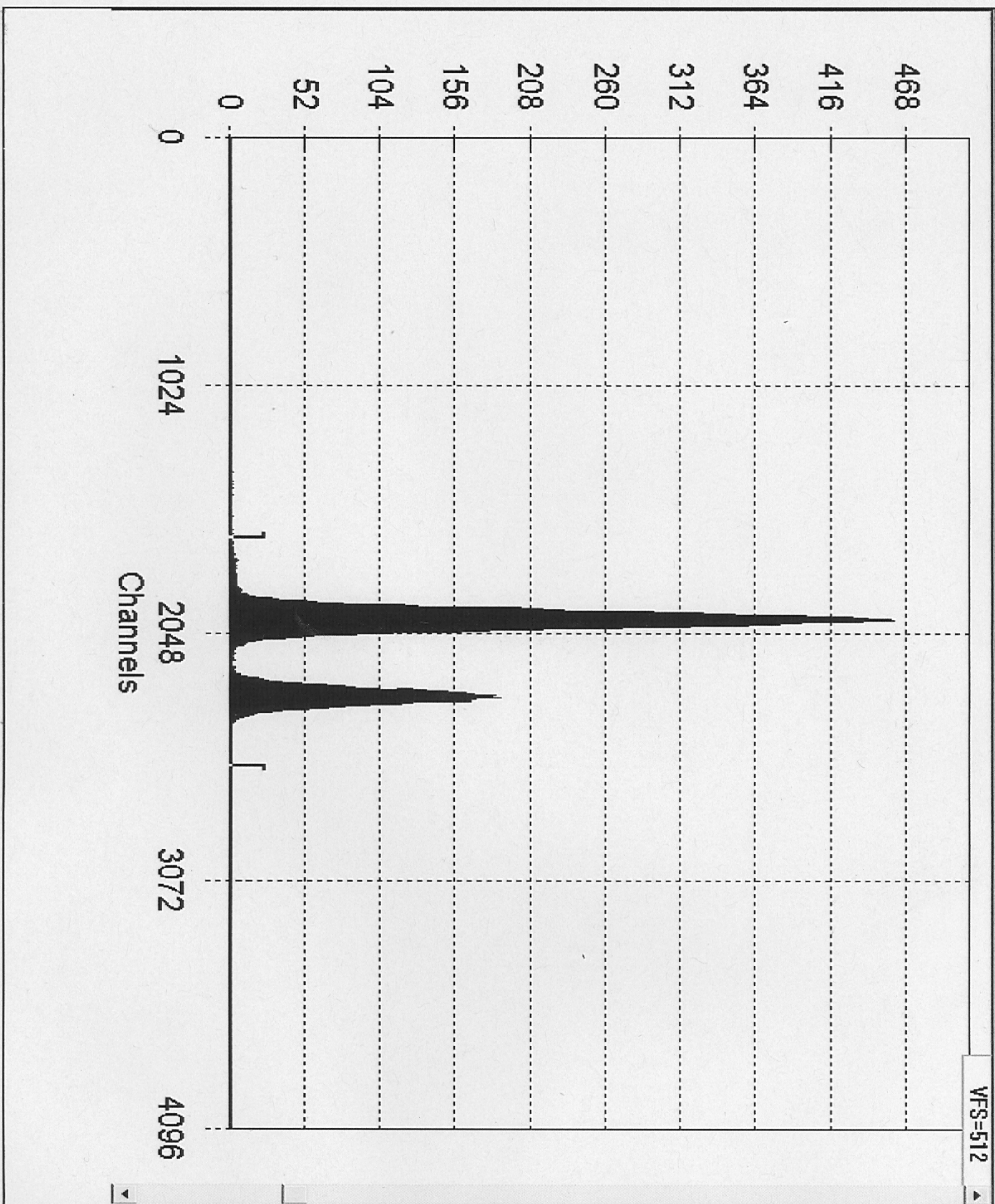
BIAS VOLTS= 12 V   Leakage   180 nA

Micron Semiconductor
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